150 MHz, 200 MS/s, 12-Bit Digitizer

NI PXI-5124

- 2 channels simultaneously sampled at 12-bit resolution
- 200 MS/s real-time and 4.0 GS/s random interleaved sampling
- 150 MHz bandwidth
- 200 mV to 20 V input range
- •>75 dBc SFDR
- 8, 32, 256, or 512 MB of memory per channel
- Edge, window, hysteresis, video, and digital triggering with 50 ps timestamping

Operating Systems

· Windows 2000/NT/XP

Recommended Software

- LabVIEW
- LabWindows/CVI
- Measurement Studio

Driver Software (included)

- NI-SCOPE driver
- LabVIEW Express VIs
- SCOPE Soft Front Panel
- NI Spectral Measurements Toolkit (with 32, 256 and 512 MB models)

Calibration

- Gain, offset, frequency response and timing self-calibration
- 2 year external calibration cycle



Overview

The National Instruments PXI-5124 high-resolution digitizer features two 200 MS/s simultaneously sampled inputs channels with 12-bit resolution, 150 MHz bandwidth, and up to 512 MB of memory per channel in a compact 3U PXI module. The NI PXI-5124 uses the high-speed PCI bus and the scatter-gather bus mastering of the NI MITE ASIC to move data to the computer at speeds up to 100 times faster than traditional instrument interfaces, thereby dramatically decreasing overall test time. With the Synchronization and Memory Core (SMC) architecture of the PXI-5124, you can create mixed-signal systems using signal generators and digital waveform generator/analyzers or build a high-channel-count digitizer with subnanosecond synchronization between channels.

Dual 200 MS/s, 12-Bit Input Channels for Time and Frequency Analysis

- 150 MHz input bandwidth with anti-alias and noise filters
- >75 dBc spurious free dynamic range (SFDR)
- 4.0 GS/s equivalent time sampling for repetitive signals
- \bullet Independent channel selectable 200 mV $_{pp}$ to 20 V_{pp} input ranges
- Independent channel selectable 50 Ω or 1 M Ω input impedance
- 2-year calibration cycle and 0 to 55 °C operating temperature

Deep Onboard Memory

- 8, 32, 256, or 512 MB of memory per channel
- Capture more than 1 million triggered waveforms with multiple record hardware rearm
- Stream data continuously from onboard memory to host memory or disk

Triggering, Clocking, and Synchronization

- Edge, window, hysteresis, video, digital, triggering with 50 ps timestamping
- Pre and posttrigger acquisition in single and multiple-record mode
- Internal 200 MHz clock or external clock from 50 to 210 MHz
- Phase lock to PXI 10 MHz reference or external reference from 1 to 20 MHz

Software

- IVI-compliant NI-SCOPE driver for LabVIEW, LabWindows/CVI, and Microsoft C++ and Visual Basic with more than 50 built-in measurements
- SCOPE Soft Front Panel for interactive control
- Spectral Measurements Toolkit for sophisticated frequency-domain measurements in communications, signal intelligence, and avionics applications

Applications Communications xDSL Wireless communications Baseband I & Q Consumer Electronics DVD, DVD-R, and PVR Set top box Gaming console Biomedical and Scientific Research Ultrasonic medical imaging Mass spectrometry Particle physics Aerospace/Defense Emulation of IC communications Consumer Electronics RADAR, SONAR, and LIDAR Satellite Signal intelligence

Ordering Information

Recommended PXI Switch

NI PXI-2593778793-0



150 MHz, 200 MS/s, 12-Bit Digitizer

Specifications-

These specifications are valid for 0 to 55 °C, unless otherwise stated.

Acquisition System

Number of channels..... 2 simultaneously sampled

Bandwidth (-3 dB)

Full Scale Input Range	50 Ω	1 M Ω
400 mV, 1 V, 2 V, 5 V, 10 V, 20 V	150 MHz	145 MHz ¹
200 mV	85 MHz	75 MHz

Bandwidth limit filters

(software selectable). 20 MHz noise (2-pole Bessel) 60 MHz antialias (4-pole elliptical) 200 MS/s real-time, 4 GS/s random interleave sampling Maximum sampling rate. 8, 32, 256, or 512 MB per channel Onboard sample memory

(4, 16, 128, 256 million samples) Pre and posttrigger data points2. 0 to 100% of full record length

	Memory per channel (MB)	Maximum number of records
Multiple records acquisition	8	32,768
(0 to 100% pre and posttrigger data)	32	131,072
	256	1,048,576
	512	2,097,152

	0.2	2,007,102
Input impodence	50 Ω and 1 M Ω II 25 pF, soft	avora polostabla
Input impedance		
Full-scale input range	50 Ω: 200 mV, 400 mV, 1 V, 2 V, 4 V, 10 V	
	1 MΩ: 200 mV, 400 mV, 1 V, 2	2 V, 4 V, 10 V, 20 V
Vertical offset ranges	±50% of full scale input range	
Maximum input overload	50 Ω: 7 V _{ms} with peaks ≤ 10 V	
	1 MΩ: peaks ≤ 42 V	
Input coupling	AC, DC, GND (AC coupling on	1 MΩ only)
AC	line sutoff frances (2 dD) 12 Hz	

Accuracy

DC accuracy (0 V offset setting)

Full Scale Input Range	50 Ω and 1 $M\Omega$
200 mV, 400 mV	±0.65% of Input ±1.3 mV
1 V, 2 V	±0.65% of Input ±1.5 mV
4 V, 10 V, 20 V	±0.65% of Input ±10.0 mV

Passband flatness (referenced at 50 kHz)

	Full-scale input range	50 Ω and 1 $M\Omega$
Filters Off	400 mV, 1 V, 2 V, 5 V, 10 V, 20 V	±0.5 dB, DC to 20 MHz
		±1 dB, 20 MHz to 50 MHz
		±1.7 dB, 50 MHz to 100 MHz
	200 mV	±0.6 dB, DC to 20 MHz
		±1.5 dB, 20 MHz to 40 MHz
Antialias Filter On	All ranges	-1 dB to +2 dB, DC to 55 MHz

AC amplitude accuracy (50 kHz) ... 1 MΩ: ±0.09 dB ≤-85 dB at 10 MHz Channel-to-channel crosstalk ...

Spectral Characteristics (typical)

Dynamic Performance (50 Ω input impedance with 10 MHz, -1 dBFS input signal)

Full Scale Input Range	SFDR	THD	SNR	SINAD
200 mV	75 dB	-74 dBc	57 dB	57 dB
400 mV	75 dB	-74 dBc	58 dB	58 dB
1 V	72 dB	-72 dBc	58 dB	58 dB
2 V	72 dB	-72 dBc	58 dB	58 dB
4 V	65 dB	-63 dBc	_	-
10 V	65 dB	-63 dBc	-	-

Dynamic Performance (1 M Ω input impedance with 10 MHz, -1 dBFS input signal)

Full Scale Input Range	SFDR	THD	SNR	SINAD
200 mV	70 dB	-68 dBc	53 dB	53 dB
400 mV	70 dB	-68 dBc	55 dB	55 dB
1 V	70 dB	-68 dBc	57 dB	57 dB
2 V	70 dB	-67 dBc	57 dB	57 dB
4 V	67 dB	-66 dBc	56 dB	56 dB
10 V	60 dB	-58 dBc	-	-
20 V	60 dB	-58 dBc	-	-

SFDR = Spurious-free dynamic range

THD = Total harmonic distortion

SNR = Signal-to-noise ratio, excluding distortion (antialias filter enabled)

SINAD = Signal-to-noise and distortion (antialias filter enabled)

RMS Noise (20 MHz filter enabled)

Full Scale Input Range	50 Ω	1 M Ω
200 mV	94 μV _{rms}	104 μV _{rms}
400 mV	188 μV _{rms}	192 μV _{rms}
1 V	470 μV _{rms}	480 μV _{rms}
2 V	940 μV _{rms}	960 μV _{rms}
4 V	1.88 mV _{ms}	1.92 mV _{rms}
10 V	4.7 mV _{rms}	4.8 mV _{rms}
20 V (1 MΩ only)	-	9.4 mV _{rms}

Intermodulation distortion3 (IMD).

Phase noise density (10 MHz input) <-100 dBc/Hz at 100 Hz

<-120 dBc/Hz at 1 kHz <-130 dBc/Hz at 10 kHz

Acquisition Modes

200 MS/s to 3.052 kS/s sample rate Real-time sampling rate... 4 GS/s to 400 MS/s sample rate (repetitive signals only) Random interleave sampling (RIS)....

Timebase System

Total sample clock jitter4

Internal

200 MS/s sampling rate with Internal sample clock frequency..... decimation by n where $1 \le n \le 65,535$ Timebase accuracy... ±25 ppm (±0.0025%)

External	
External clock sources	CLK IN (SMB connector), PXI star
External clock range	50 MHz to 210 MHz, variable with
	decimation by n where $1 \le n \le 65,535$
External reference sources	CLK IN (SMB connector), PXI backplane 10 MHz
External reference range	1 to 20 MHz in 1 MHz increments
External clock/reference amplitude	Sine wave: 0.65 to 2.8 Vpp (0 to 13 dBm)
	Square wave: 0.2 to 2.8 Vpp
External clock/reference impedance	50 Ω, AC coupled

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Modes	Edge, Hysteresis, Window, Video, Digital, Immediate, Software
Source	CH 0, CH 1, TRIG, PXI_Trig <0:6>, PFI <0:1>, PXI Star, Software
Slope	Rising or falling
Hysteresis	Fully programmable
Video trigger	Negative sync of NTSC, PAL, and SECAM standards
Video trigger types	Any line, specific line, specific field
High frequency reject filter	50 kHz software selectable
Low frequency reject filter	50 kHz software selectable
Sensitivity	CHO and CH1: 2.5% FS up to 50 MHz
	decreasing to 10% FS at 150 MHz
	TRIG: 2.5% up to 100 MHz decreasing to 10% at 200 MHz
Level accuracy	CH0, CH1: ±4.7% FS up to 10 MHz
	TRIG: ±3.5% FS up to 10 MHz
Time resolution	50 ps with time-to-digital converter enabled
Holdoff ⁵	2 μs to 171.79 s, software selectable

External Trigger Channel (TRIG)

Impedance	1 MΩ II 22 pF
Vertical Range	±5 V
Coupling	AC, DC

Power Requirements (typical)

+3.3 VDC	+5 VDC	+12 VDC	-12 VDC	Total Power
1.3 A	1.7 A	130 mA	270 mA	17.6 W

Environment

Operating temperature ⁶	0 to 55 °C (Meets IEC-60068-2-1 and IEC-60068-2-2)
Storage temperature	-40 to 71 °C (meets IEC-60068-2-1 and 60068-2-2)
Relative humidity	10 to 90 %, noncondensing (meets 60068-2-56)
Calibration	
Self-calibration	Gain, offset, frequency response,

triggering, and timing for all input ranges

External calibration interval... Certification and Compliances (€

For detailed specifications on power, environmental, safety, and physical dimensions see PXI-5124 detailed specifications.

- 1.Bandwidth on the 1 M Ω input is 145 MHz for 0 to 40 $^{\circ}$ C and 135 MHz for 40 to 55 $^{\circ}$ C
- All PRJ-S124 is also capable of continous acquisition

 3. Measured on ranges up to 2 V on 50 Ω input with two tones at 10.2 MHz and 11.2 MHz, each at -7 dBFS

 4. Includes effects of converter aperture and clock circuitry jitter from 100 Hz to 100 kHz
- 5. Time-digital converter disabled 6. 0 to 45 °C in PXI-101x and 1000/B chassis

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