TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (L^2 - π -MOSV)

2SJ525

Chopper Regulator, DC-DC Converter and Motor Drive Applications

• 4-V gate drive

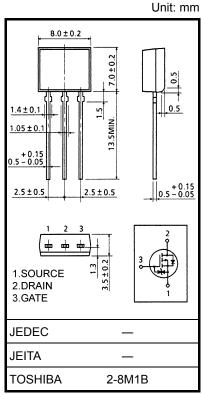
• Low drain-source ON resistance : $R_{DS (ON)} = 0.1 \Omega (typ.)$ • High forward transfer admittance : $|Y_{fs}| = 4.5 S (typ.)$

• Low leakage current : $I_{DSS} = -100 \mu A \text{ (max) (V}_{DS} = -30 \text{ V)}$

• Enhancement mode : $V_{th} = -0.8$ to -2.0 V ($V_{DS} = -10$ V, $I_D = -1$ mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteris	stics	Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	-30	V
Drain-gate voltage (R _{GS} = 20 kΩ)		V_{DGR}	-30	V
Gate-source voltage		V _{GSS}	±20	V
Drain current	DC (Note 1)	ID	-5	Α
Diam current	Pulse (Note 1)	I _{DP}	-20	Α
Drain power dissipation	١	P_{D}	1.3	W
Single pulse avalanche energy (Note 2)		E _{AS}	517	mJ
Avalanche current		I _{AR}	-5	Α
Repetitive avalanche e	nergy (Note 3)	E _{AR}	0.13	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature range		T _{stg}	-55~150	°C



Weight: 0.54 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient	R _{th (ch-a)}	96.1	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = -25 V, T_{ch} = 25°C (initial), L = 14.84 mH, R_G = 25 Ω , I_D = -5 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.

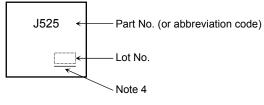
Electrical Characteristics (Ta = 25°C)

Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	irrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	_	±10	μΑ
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = -30 V, V _{GS} = 0 V	_	_	-100	μΑ
Drain-source br voltage	eakdown	V _{(BR) DSS}	I _D = -10 mA, V _{GS} = 0 V	-30	_	_	V
Gate threshold v	voltage	V_{th}	$V_{DS} = -10 \text{ V}, I_{D} = -1 \text{ mA}$	-0.8	_	-2.0	V
Drain-source O	N resistance	Pro (ou)	V _{GS} = -4 V, I _D = -2.5 A	_	0.17	0.2	Ω
Drain-source ON resistance		R _{DS} (ON)	V _{GS} = -10 V, I _D = -2.5 A —		0.1	0.12	12
Forward transfer	r admittance	Y _{fs}	V _{DS} = -10 V, I _D = -2.5 A	2.0	4.5	_	S
Input capacitano	e	C _{iss}		_	850	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = -10 V, V _{GS} = 0 V, f = 1 MHz	_	250	_	pF
Output capacitance		Coss			330	_	
Switching time	Rise time	t _r	V_{GS} V_{DD} V_{DD} V_{DD} V_{DD} V_{DD} V_{DD}	_	50	_	
	Turn-on time	t _{on}		_	75	_	ne
	Fall time	t _f		ı	20	_	ns
	Turn-off time	t _{off}		ı	95	_	
Total gate charge (Gate-source plus gate-drain)		Qg	V _{DD} ≈ -24 V, V _{GS} = -10 V,	_	27	_	
Gate-source charge		Q_{gs}	$I_D = -5 \text{ A}$		19	_	nC
Gate-drain ("miller") charge		Q_{gd}		_	8	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	-5	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	-	_	-20	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = -5 A, V _{GS} = 0 V	_	_	1.7	V
Reverse recovery time	t _{rr}	I _{DR} = -5 A, V _{GS} = 0 V		60	_	ns
Reverse recovery charge	Q_{rr}	dl _{DR} / dt = 50 A / μs	-	56	_	nC

Marking



Note 4: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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