

HD74LS145

BCD-to-Decimal Decoder / Driver (with 15 V outputs)

REJ03D0436-0300 Rev.3.00 Jul.13.2005

This BCD-to-decimal decoder / driver consists of eight inverters and ten four-input NAND gates. The inverters are connected in pairs to make BCD input data available for decoding by the NAND gates. Full decoding of valid BCD input logic ensures that all outputs remain off for all invalid binary input conditions. This decoder features high-performance, n-p-n output transistors designed for use as indicator / relay drivers or as open-collector logic-circuit drivers.

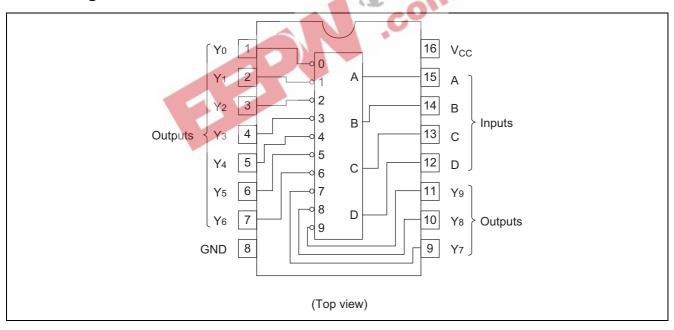
Features

Ordering Information

Part Name	rt Name Package Type		Package Abbreviation	Taping Abbreviation (Quantity)	
HD74LS145P	DILP-16 pin	PRDP0016AE-B (DP-16FV)	Р	_	
HD74LS145FPEL	SOP-16 pin (JEITA)	PRSP0016DH-B (FP-16DAV)	FP 4	EL (2,000 pcs/reel)	

Note: Please consult the sales office for the above package availability.

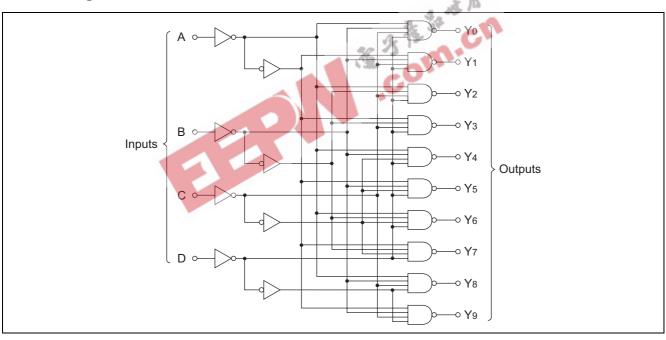
Pin Arrangement



Function Table

No.	Inputs				Outputs									
NO.	D	С	В	Α	0	1	2	3	4	5	6	7	8	9
0	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н
1	L	L	L	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	Н
2	L	L	Н	L	Н	Н	L	Н	Н	Н	Н	Н	Н	Н
3	L	L	Н	Н	Н	Н	Н	L	Н	Н	Н	Н	Н	Н
4	L	Н	L	L	Н	Н	Н	Н	L	Н	Н	Н	Н	Н
5	L	Н	L	Н	Н	Н	Н	Н	Н	L	Н	Н	Н	Н
6	L	Н	Н	L	Н	Н	Н	Н	Н	Н	L	Н	Н	Н
7	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L	Н	Н
8	Н	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	L	Н
9	Н	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L
	Н	L	Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
	Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
Invalid	Н	Н	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
ilivaliu	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
	Η	Н	Η	L	Н	Н	Н	Н	Н	Н	Η	Н	Н	Н
	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н

Block Diagram



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply voltage	Vcc	7	V
Input voltage	V_{IN}	7	V
Power dissipation	P_{T}	400	mW
Storage temperature	Tstg	-65 to +150	°C

Note: Voltage value, unless otherwise noted, are with respect to network ground terminal.

Recommended Operating Conditions

Item	Symbol	Min	Тур	Max	Unit
Supply voltage	V_{CC}	4.75	5.00	5.25	V
Off-state output voltage	$V_{O(off)}$	_	_	15	V
Low level output current	I _{OL}	_	_	80	mA
Operating temperature	Topr	-20	25	75	°C

Electrical Characteristics

 $(Ta = -20 \text{ to } +75 \text{ }^{\circ}\text{C})$

Item	Symbol	min.	typ.*	max.	Unit	Condition
Input voltage	V _{IH}	2.0	_	_	V	
Input voltage	V _{IL}	_	_	0.8	V	
Off-state output current	I _{O(off)}	_	_	250	μΑ	$V_{CC} = 4.75 \text{ V}, V_{IH} = 2 \text{ V}, V_{IL} = 0.8 \text{ V}, V_{O(off)} = 15 \text{ V}$
On state systems	V _{O(on)}	_	_	0.4	V	I _{OL} = 12 mA
On-state output voltage		_	_	0.5		$V_{CC} = 4.75 \text{ V}, V_{IH} = 2 \text{ V},$ $V_{IL} = 0.8 \text{ V}$
Voltage		_	_	3.0		I _{OL} = 80 mA
	I _{IH}	_	_	20	μΑ	$V_{CC} = 5.25 \text{ V}, V_{I} = 2.7 \text{ V}$
Input current	I _{IL}	_	_	-0.4	mA	$V_{CC} = 5.25 \text{ V}, V_I = 0.4 \text{ V}$
	II	_	_	0.1	mA	$V_{CC} = 5.25 \text{ V}, V_I = 7 \text{ V}$
Supply current**	Icc	_	7	13	mA	$V_{CC} = 5.25 \text{ V}$
Input clamp voltage	V _{IK}	_	_	-1.5	V	$V_{CC} = 4.75 \text{ V}, I_{IN} = -18 \text{ mA}$

Notes: $^*V_{CC} = 5 \text{ V}$, $Ta = 25^{\circ}C$

Switching Characteristics

 $(V_{CC} = 5 \text{ V}, \text{Ta} = 25^{\circ}\text{C})$

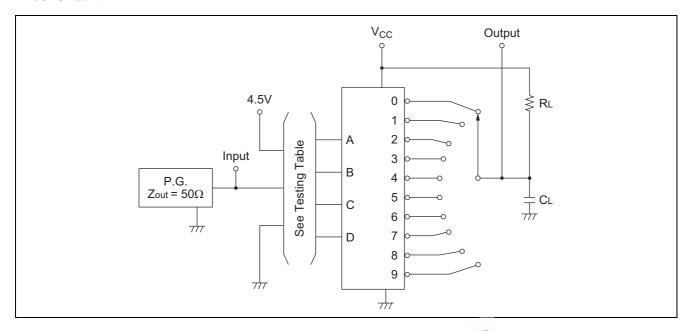
Item	Symbol	min.	typ.	max.	Unit	Condition
Propagation delay time	t _{PLH}		_	50	ne	$C_L = 45 \text{ pF}, R_L = 665 \Omega$
Fropagation delay time	tehl	_	_	50	ns	C _L = 45 βF, R _L = 005 Ω

^{**} I_{CC} is measured with all outputs open and all inputs grounded.

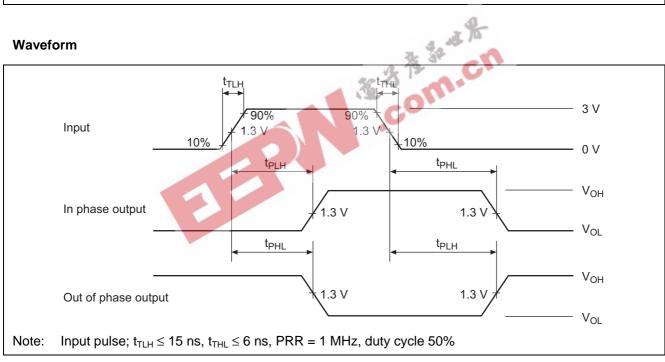
*hing Characteristics

Testing Method

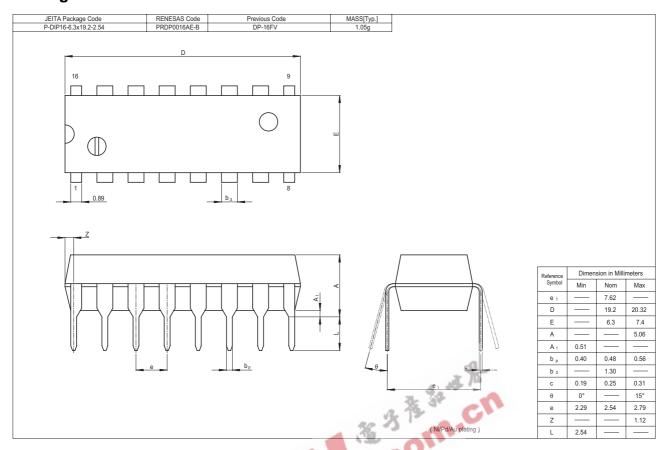
Test Circuit

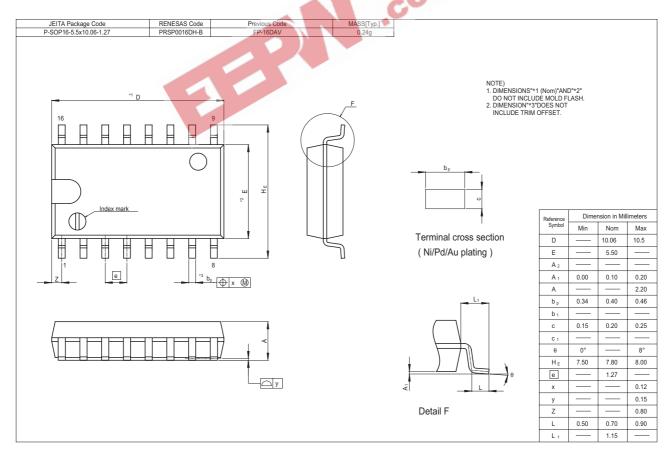


Waveform



Package Dimensions





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