



4Mx32 FLASH MODULE

DESCRIPTION

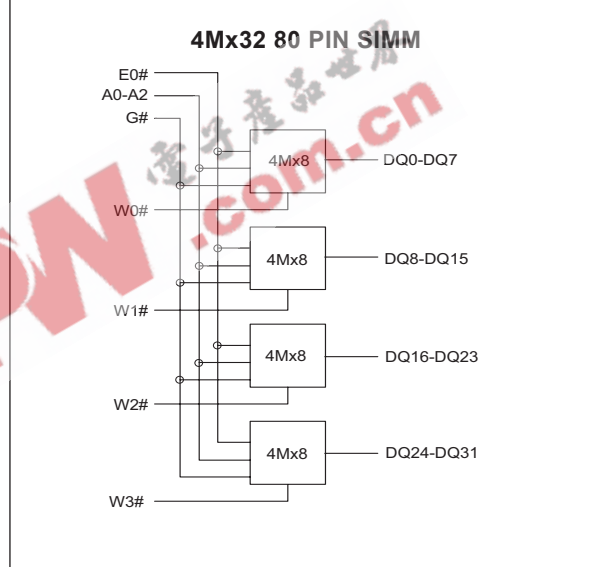
The WED7F324XE3SN is organized as a 4Mx32 flash module. The module is based on AMDs AM29LV033C - 4Mx8 Flash device in TSOP packages which are mounted on an FR4 substrate.

The module offers access times between 70 and 120ns allowing for operation of high-speed microprocessors without wait states.

FEATURES

- 4Mx32
- Based on AMD - AM29LV033C Flash Device
- Fast Read Access Time - 80ns
- 3.3V-Only Reprogramming
- Single power supply operation
 - Regulated voltage range: 3.0V to 3.6V read and write operations and for compatibility with high performance 3.3V microprocessors.
- Flexible sector architecture
 - Sixty-four 64 Kbyte sectors
- High performance
 - Access times as fast as 70ns
 - Program time: 7 μ s/byte typical utilizing Accerlate function
- Minimum 1 million write cycles guaranteed per sector
- Supports Common Flash Memory Interface (CFI)
- Erase Suspend/Erase Resume
 - Suspends erase operations to allow programming in same bank
- Data# Polling and Toggle Bits
 - Provides a software method of detecting the status of program or erase cycles
- Commercial and Industrial Temperature Range
- Package
 - 80 PIN SIMM (JEDEC)

FIG. 1
BLOCK DIAGRAM





CAPACITANCE

f = 1.0MHz, V_{IN} = V_{CC} or V_{SS})

Parameter	Sym	4Meg	
		Max	Unit
Address Lines	CA	35	pF
Data lines	CDQ	15	pF
Chip & Write Enable Lines	CC	15	pF
Output Enable lines	CG	35	pF

Pin Configurations

Pin #	Pin Name	Pin #	Pin Name	Pin #	Pin Name	Pin #	Pin Name
1	V _{SS}	21	*	41	A11	61	DQ9
2	V _{CC}	22	*	42	A10	62	DQ8
3	NC	23	*	43	A9	63	DQ7
4	G#	24	*	44	A8	64	DQ6
5	W0#	25	V _{SS}	45	A7	65	DQ5
6	W1#	26	DQ29	46	A6	66	DQ4
7	NC	27	DQ30	47	A5	67	DQ3
8	DQ16	28	DQ31	48	A4	68	DQ2
9	DQ17	29	W2#	49	A3	69	DQ1
10	DQ18	30	NC	50	A2	70	DQ0
11	DQ19	31	A21	51	A1	71	NC
12	DQ20	32	A20	52	AO	72	V _{CC}
13	DQ21	33	A19	53	W3#	73	PD1
14	DQ22	34	A18	54	V _{SS}	74	PD2
15	DQ23	35	A17	55	DQ15	75	PD3
16	DQ24	36	A16	56	DQ14	76	PD4
17	DQ25	37	A15	57	DQ13	77	PD5
18	DQ26	38	A14	58	DQ12	78	PD6
19	DQ27	39	A13	59	DQ11	79	PD7
20	DQ28	40	A12	60	DQ10	80	V _{SS}

*TBD

SIMM Density	
Pin	4Meg
21	NC
22	NC
23	NC
24	E0#

Presence Detect Pin Out	
Pin	2Meg
PD1	V _{SS}
PD2	NC
PD3	NC
PD4	NC

- A0-A21 Address input
- E0# Chip Enable
- W0#-W3# Write Enable
- G# Output Enable
- DQ0-DQ31 Data Input/Output
- P_D Presence Detect
- V_{CC} Power 3.3V±10%
- V_{SS} Ground
- NC No Connect

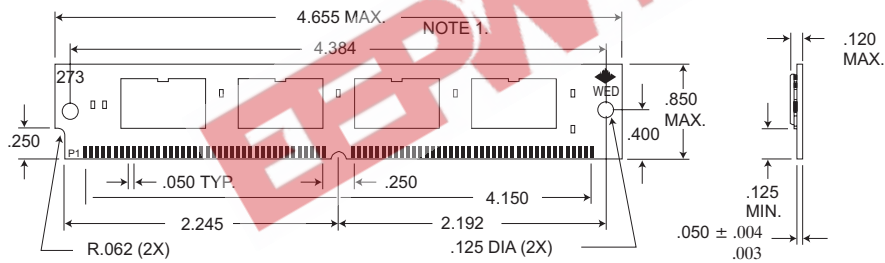


Ordering Information

Part Number	Speed (ns)	Package
WED7F324XE3SN70C	70	346
WED7F324XE3SN90C	90	346
WED7F324XE3SN100C	100	346

Note: To order an Industrial grade product substitute the letter C in the Suffix with the letter I.

PACKAGE NO. 346: 80 PIN SIMM (JEDEC)



ALL DIMENSIONS ARE IN INCHES