

### **SD56120**

## RF POWER TRANSISTORS The *LdmoST* FAMILY

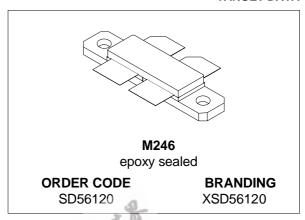
#### **TARGET DATA**

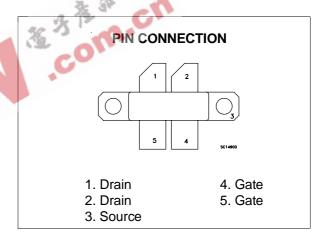
# N-CHANNEL ENHANCEMENT-MODE LATERAL MOSFETs

- EXCELLENT THERMAL STABILITY
- COMMON SOURCE CONFIGURATION, PUSH-PULL
- P<sub>OUT</sub> = 100 W PEP WITH 13 dB GAIN @ 860 MHz
- BeO FREE PACKAGE

#### **DESCRIPTION**

The SD56120 is a common source N-Channel enhancement-mode lateral Field-Effect RF power transistor designed for broadband commercial and industrial applications at frequencies up to 1.0 GHz. The SD56120 is designed for high gain and broadband performance operating in common source mode at 28V. It is ideal for broadcast applications from 470 to 860 MHz requiring high linearity.





### **ABSOLUTE MAXIMUM RATINGS** (T<sub>case</sub> = 25 °C)

Symbol	Parameter	Value	Unit
V <sub>(BR)DSS</sub>	Drain Source Voltage	65	V
$V_{GS}$	Gate-Source Voltage	± 20	V
I <sub>D</sub>	Drain Current	14	А
P <sub>DISS</sub>	Power Dissipation (@ Tc= 70°C)	260	W
Tj	Max. Operating Junction Temperature	200	°C
T <sub>STG</sub>	Storage Temperature	-65 to 150	°C

#### THERMAL DATA

R <sub>th(j-c)</sub> Junction-Case Thermal Resistance	0.5	°C/W
---	-----	------

November 1999 1/4

# **ELECTRICAL SPECIFICATION** $(T_{case} = 25 \degree C)$

### STATIC (Per Section)

Symbol		Parameter		Min.	Тур.	Max.	Unit
$V_{(BR)DSS}$	$V_{GS} = 0V$	$I_{DS} = 10 \text{ mA}$		65			V
$I_{DSS}$	$V_{GS} = 0V$	$V_{DS} = 28 V$				1	μΑ
$I_{GSS}$	$V_{GS} = 20V$	$V_{DS} = 0 V$				1	μΑ
$V_{GS(Q)}$	$V_{DS} = 28V$	$I_D = 100 \text{ mA}$		3.0		5.0	V
V <sub>DS(ON)</sub>	$V_{GS} = 10V$	$I_D = 3 A$			0.7	0.8	V
$G_FS$	$V_{DS} = 10V$	$I_D = 3 A$			3		mho
$C_{ISS}$	$V_{GS} = 0V$	$V_{DS} = 28 V$	f = 1 MHz		88		pF
Coss	$V_{GS} = 0V$	$V_{DS} = 28 \text{ V}$	f = 1 MHz		44		pF
C <sub>RSS</sub>	$V_{GS} = 0V$	$V_{DS} = 28 V$	f = 1 MHz		1.7		pF

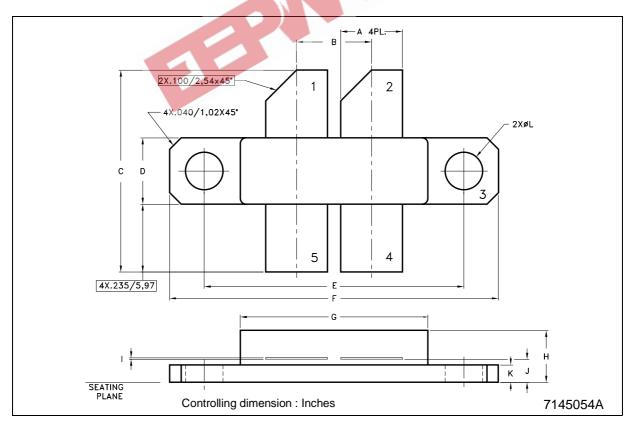
### **DYNAMIC**

Symbol	Parameter			Min.	Тур.	Max.	Unit
Pout	$V_{DD} = 28V$	f = 860 MHz	I <sub>DQ</sub> = 400 mA	100			W
G <sub>PS</sub>	$V_{DD} = 28 \text{ V}$	P <sub>out</sub> = 100W PEP	I <sub>DQ</sub> = 400 mA	13			dB
ηD	$V_{DD} = 28 \text{ V}$	Pout = 100W PEP	I <sub>DQ</sub> = 400 mA	30	36		%
IMD	$V_{DD} = 28 \text{ V}$	P <sub>out</sub> = 100W PEP	$I_{DQ} = 400 \text{ mA}$		31		dB
Load Mismatch		V <sub>DD</sub> = 28 V ALL PHASE AN		5:1			VSWR
Note: $f_1 = 860$ $f_2 = 860$							

2/4

# M246 (.230 x .650 WIDE 4/L BAL N/HERM W/FLG) MECHANICAL DATA

DIM.	mm			inch		
2.1111	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
Α	5.33		5.59	0.210		0.220
В	6.48		6.73	0.255		0.265
С	17.27		18.29	0.680		0.720
D	5.72		5.97	0.225		0.235
E		22.86			0.900	
F	28.83		29.08	1.135		1.145
G	16.26		16.76	0.640		0.660
Н	4.19		5.08	0.165	0	0.200
ı	0.08		0.15	0.003	15	0.006
J	1.83		2.24	0.072	CU	0.088
K	1.40		1.65	0.055		0.065
L	3.18		3.43	0.125		0.135





Information furnished is believed to be accurate and reliable. However, STMicroelectronics assumes no responsibility for the consequences of use of such information nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of STMicroelectronics. Specification mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information previously supplied. STMicroelectronics products are not authorized for use as critical components in life support devices or systems without express written approval of STMicroelectronics.

The ST logo is a trademark of STMicroelectronics

© 1999 STMicroelectronics – Printed in Italy – All Rights Reserved STMicroelectronics GROUP OF COMPANIES

Australia - Brazil - China - Finland - France - Germany - Hong Kong - India - Italy - Japan - Malaysia - Malta - Morocco - Singapore - Spain - Sweden - Switzerland - United Kingdom - U.S.A.

http://www.st.com

47/