

ZXTP19060CZ 60V PNP medium transistor in SOT89

Summary

 $BV_{CEO} > -60V$

 $BV_{ECO} > -7V$

 $I_{C(cont)} = 4.5A$

 $V_{CE(sat)} < -80 \text{mV} @ -1 \text{A}$

 $R_{CE(sat)} = 50m\Omega$

 $P_{D} = 2.4W$

Complementary part number ZXTN19060CZ

Description

Packaged in the SOT89 outline this new low saturation PNP transistor offers extremely low on state losses making it ideal for use in DC-DC circuits and various driving and power management functions.

Features

- · High gain
- · Low saturation voltage
- High peak current
- 7V reverse blocking voltage

Applications

- · High side driver
- Motor drive
- · Load disconnect switch

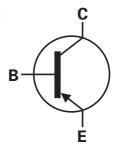
Ordering information

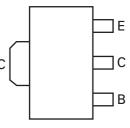
Device	Reel size (inches)	Tape width (mm)	Quantity per reel	
ZXTP19060CZTA	7	12	1000	

Device marking

1M2







Pinout - top view

Absolute maximum ratings

Parameter	Symbol	Limit	Unit
Collector-Base voltage	V _{CBO}	-60	V
Collector-Emitter voltage	V _{CEO}	-60	V
Emitter-Collector voltage (reverse blocking)	V _{ECX}	-7	V
Emitter-Base voltage	V _{EBO}	-7	V
Continuous Collector current ^(c)	I _C	-4.5	Α
Base current	I _B	-1	Α
Peak pulse current	I _{CM}	-7	Α
Power dissipation at T _A =25°C ^(a)	P _D	1.1	W
Linear derating factor		8.8	mW/°C
Power dissipation at T _A =25°C ^(b)	PD	1.8	W
Linear derating factor	4. 通用	14.4	mW/°C
Power dissipation at T _A =25°C ^(c)	P _D	2.4	W
Linear derating factor	W.	19.2	mW/°C
Power dissipation at T _A =25°C ^(d)	P_{D}	4.46	W
Linear derating factor		35.7	mW/°C
Power dissipation at T _C =25°C ^(e)	P _D	26.7	W
Linear derating factor		213	mW/°C
Operating and storage temperature range	T _j , T _{stg}	-55 to 150	°C

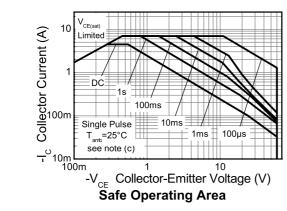
Thermal resistance

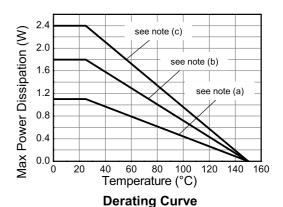
Parameter	Symbol	Limit	Unit
Junction to ambient ^(a)	$R_{\Theta JA}$	117	°C/W
Junction to ambient ^(b)	$R_{\Theta JA}$	68	°C/W
Junction to ambient ^(c)	$R_{\Theta JA}$	51	°C/W
Junction to ambient ^(d)	$R_{\Theta JA}$	28	°C/W
Junction to case ^(e)	$R_{\Theta JC}$	4.69	°C/W

NOTES:

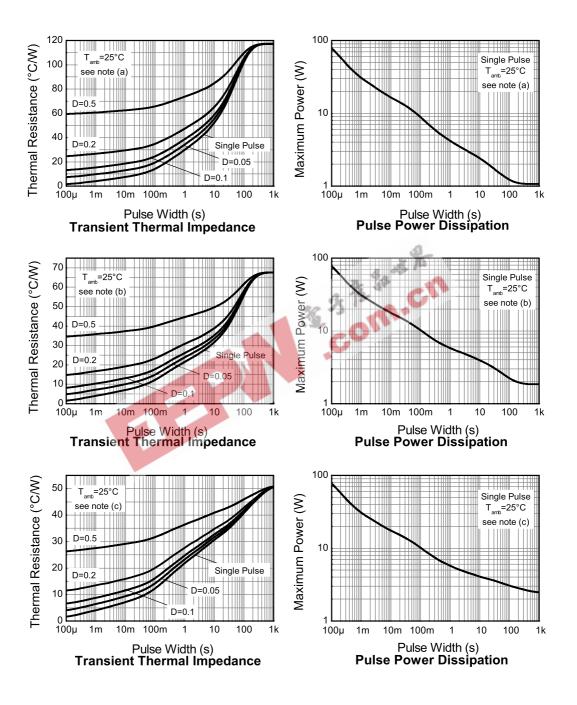
- (a) For a device surface mounted on 15mm x 15mm x 0.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.
- (b) Mounted on 25mm x 25mm x 0.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.
- (c) Mounted on 50mm x 50mm x 0.6mm FR4 PCB with high coverage of single sided 2oz copper, in still air conditions.
- (d) As (c) above measured at t<10 seconds.
- (e) Junction to case (collector tab). Typical

Thermal characteristics





Thermal characteristics



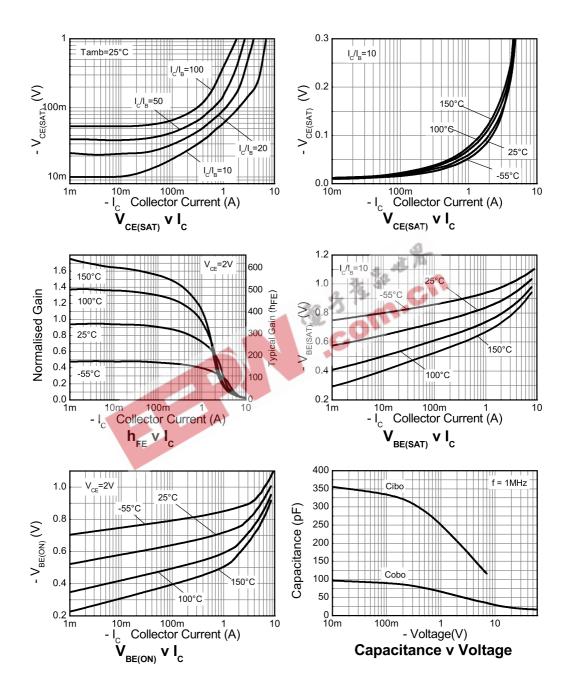
Electrical characteristics (at T_{amb} = 25°C unless otherwise stated)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-Base breakdown voltage	BV _{CBO}	-60	-110		V	$I_C = -100 \mu A$
Collector-Emitter breakdown voltage	BV _{CEO}	-60	-90		V	I _C = -10mA ^(*)
Emitter-Collector breakdown voltage (reverse blocking)	BV _{ECX}	-7	-8.4		V	I_E = -100μA, R_{BC} < 1kΩ or 0.25V > V_{BC} > -0.25V
Emitter-Collector breakdown voltage (reverse blocking)	BV _{ECO}	-7	-8.8		V	$I_E = -100\mu A$
Emitter-Base breakdown voltage	BV _{EBO}	-7	-8.4		V	I _E = -100μA
Collector-Base cut-off	I _{CBO}		<1	-50	nA	V _{CB} = -60V
current				-0.5	μΑ	$V_{CB} = -60V, T_{amb} = 100^{\circ}C$
Emitter cut-off current	I _{EBO}		<1	-50	nA	V _{EB} = -5.6V
Collector-Emitter	V _{CE(sat)}		-62	-80	mV	$I_C = -1A$, $I_B = -100 \text{mA}^{(*)}$
saturation voltage			-150	-205	mV	$I_C = -1A$, $I_B = -20mA^{(*)}$
		. 1	-500	-750	mV	$I_C = -2A$, $I_B = -40mA^{(*)}$
			-105	-165	mV	$I_C = -2A$, $I_B = -200 \text{mA}^{(*)}$
		1	- 1 45	-200	mV	$I_C = -3A$, $I_B = -300 \text{mA}^{(*)}$
			-240	-410	mV	$I_C = -4.5A$, $I_B = -450 \text{mA}^{(*)}$
Base-Emitter saturation voltage	V _{BE(sat)}		-965	-1050	mV	$I_C = -4.5A$, $I_B = -450 \text{mA}^{(*)}$
Base-Emitter turn-on voltage	V _{BE(on)}		-875	-1000	mV	$I_C = -4.5A$, $V_{CE} = -2V^{(*)}$
Static forward current	h _{FE}	200	330	500		$I_C = -100 \text{mA}, V_{CE} = -2V^{(*)}$
transfer ratio		160	260			$I_C = -1A$, $V_{CE} = -2V^{(*)}$
		25	45			$I_C = -4.5A, V_{CE} = -2V^{(*)}$
Transition frequency	f _T		180		MHz	$I_C = -50 \text{mA}, V_{CE} = -10 \text{V}$ f = 50MHz
Input capacitance	C _{ibo}		280	400	pF	$V_{EB} = -0.5V, f = 1MHz^{(*)}$
Output capacitance	C _{obo}		29.5	40	pF	$V_{CB} = -10V, f = 1MHz^{(*)}$
Delay time	t _d		24.3		ns	
Rise time	t _r		13.2		ns	$I_C = -500 \text{mA}, V_{CC} = -10 \text{V},$
Storage time	t _s		456		ns	$I_{B1} = -I_{B2} = -50 \text{mA}$
Fall time	t _f		68.2		ns	

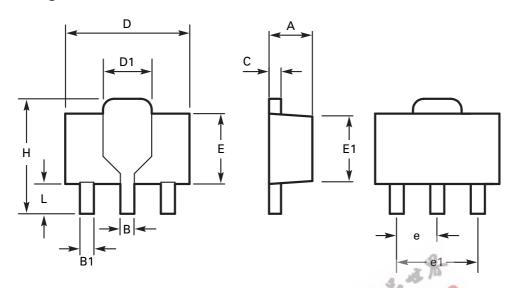
NOTES: (*) Measured under pulsed conditions. Pulse width $\leq 300 \mu s;$ duty cycle $\leq 2\%.$



Typical characteristics



Package outline - SOT89



DIM	Millin	neters	Inches		DIM Millimeters		Inches		
	Min	Max	Min	Max	32	Min	Max	Min	Max
Α	1.40	1.60	0.550	0.630	EC	2.29	2.60	0.090	0.102
В	0.44	0.56	0.017	0.022	E1	2.13	2.29	0.084	0.090
B1	0.36	0.48	0.014	0.019	е	1.50	BSC	0.059	BSC
С	0.35	0.44	0.014	0.017	e1	3.00	BSC	0.118	BSC
D	4.40	4.60	0.173	0.181	Н	3.94	4.25	0.155	0.167
D1	1.52	1.83	0.064	0.072	Ĺ	0.89	1.20	0.035	0.047

Note: Controlling dimensions are in millimeters. Approximate dimensions are provided in inches

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